

BIPOLAR JUNCTION TRANSISTOR AND FABRICATING METHOD

Appl. No.	:	10/709,568	Confirmation No.	3567
Applicant	:	Anchor Chen		
Filed	:	May 14, 2004		
TC/A.U.	:	2818		
Examiner	:	Luu, Chuong A		
Docket No.	:	NAUP0486USA4		
Customer No.	:	27765		

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

AMENDMENT

Sir:

- 5 In response to the Office action of April 19, 2007, please amend the above-identified application as follows:

Listing of the Claims begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.